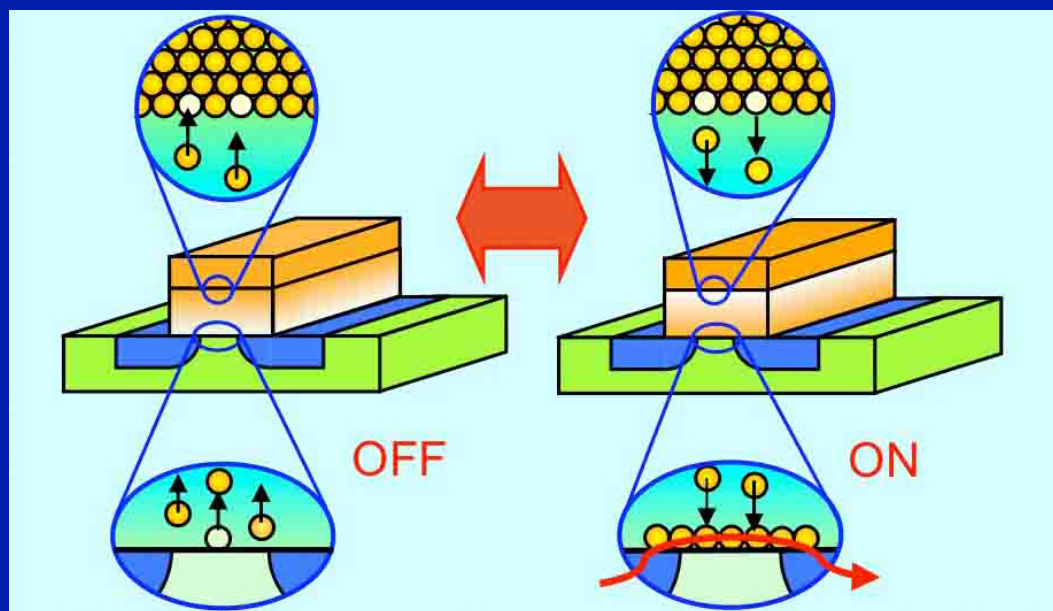


# Atom/ion movement controlled three-terminal atomic switch, 'Atom Transistor'



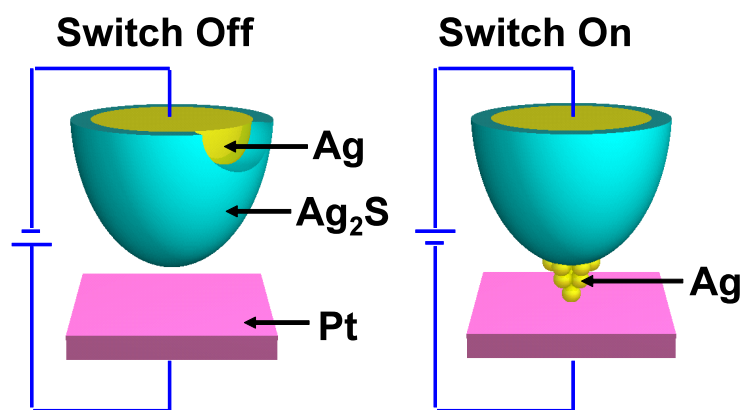
Tsuyoshi Hasegawa

WPI Center for Materials Nanoarchitectonics (MANA),  
National Institute for Materials Science (NIMS)

# Two-terminal atom movement controlled device: Atomic Switch

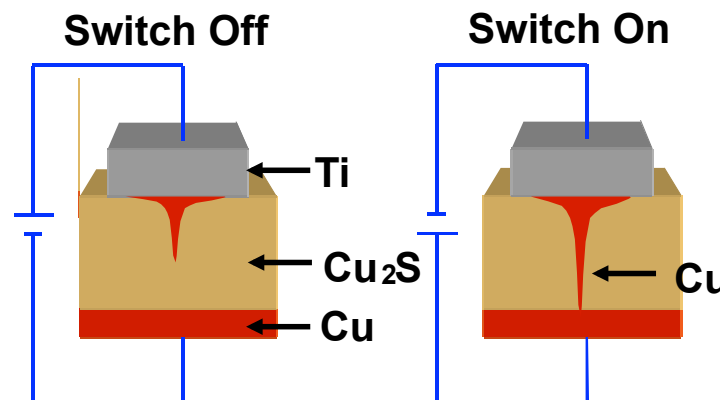
Metal filament formation and annihilation is controlled by solid-electrochemical reaction.

Metal cations are controlled.



Initial type of Atomic switch

*K. Terabe et al.,  
Riken Review 37 (2001) 7.*

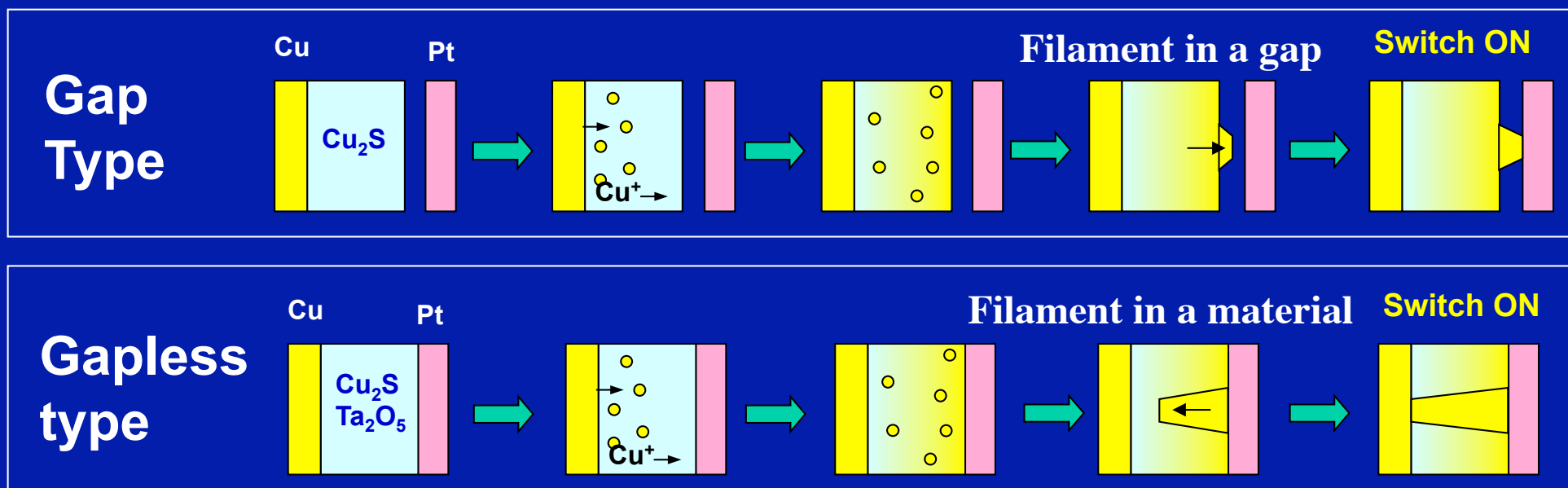


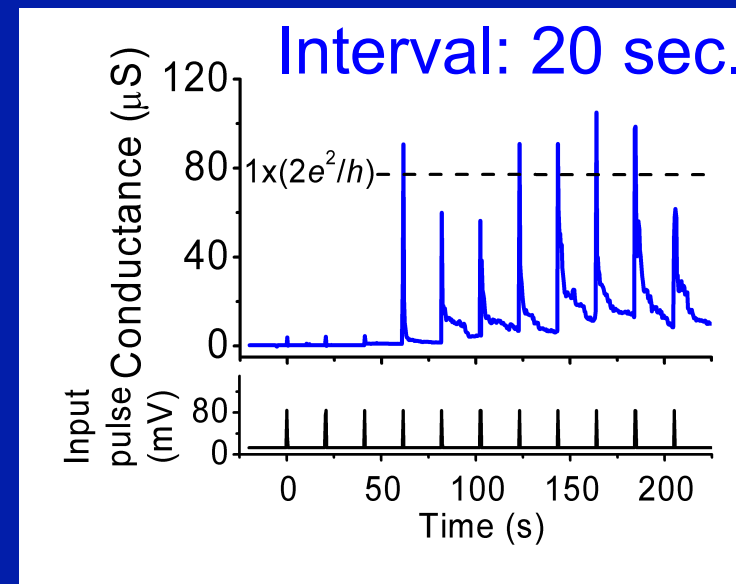
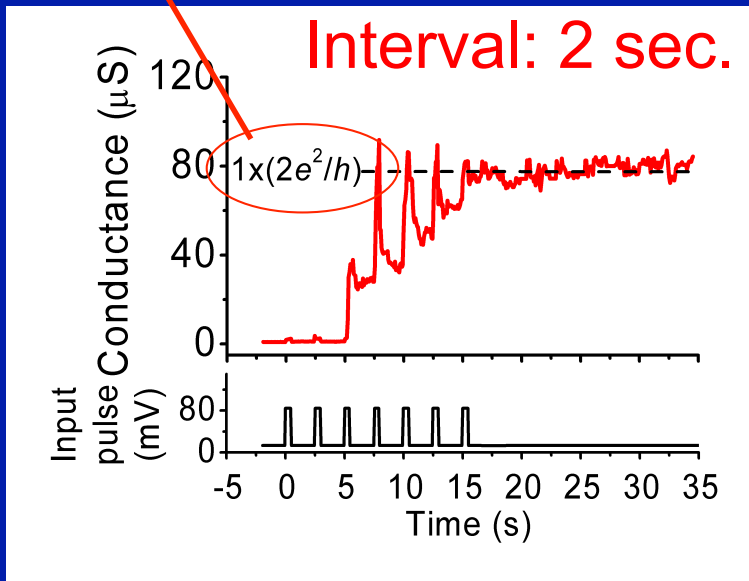
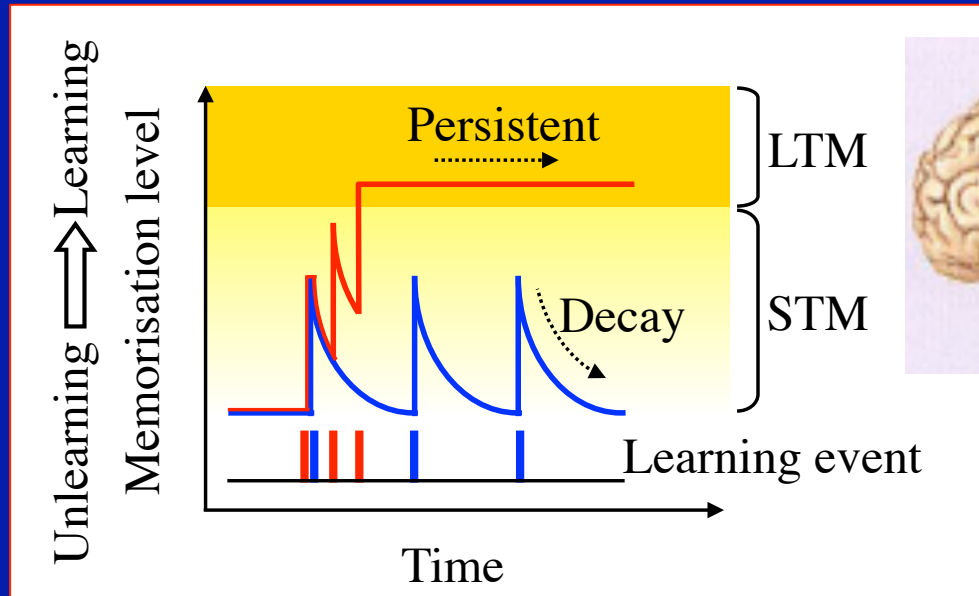
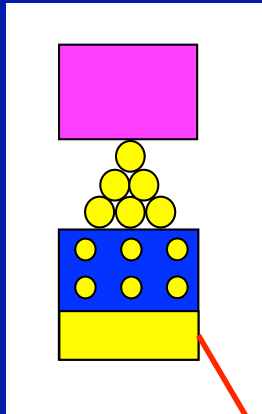
Gapless atomic switch (NanoBridge™)

*T. Sakamoto et al.,  
Appl. Phys. Lett. 82 (2003) 3032.*

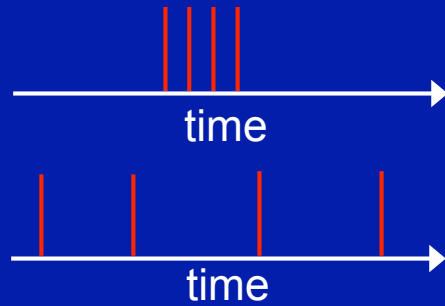
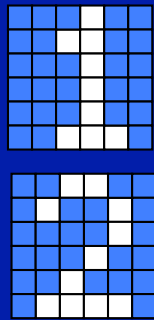
# Operating mechanism of the atomic switches

Diffusion of Metal Cations+Reduction/Oxidation  
(Demonstrated by  $Ta_2O_5$ ,  $Nb_2O_5$ ,  $HfO$ ,  $Ag_2S$ ,  $Cu_2S$ , etc.)

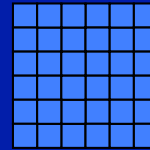




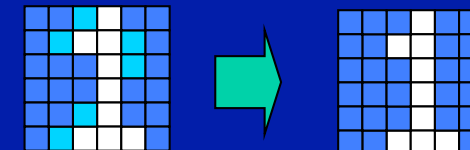
## Input Patterns



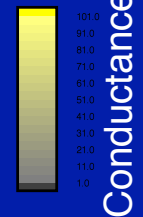
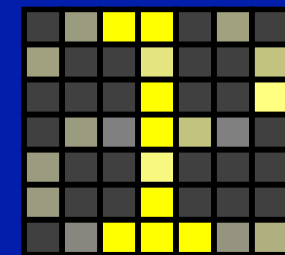
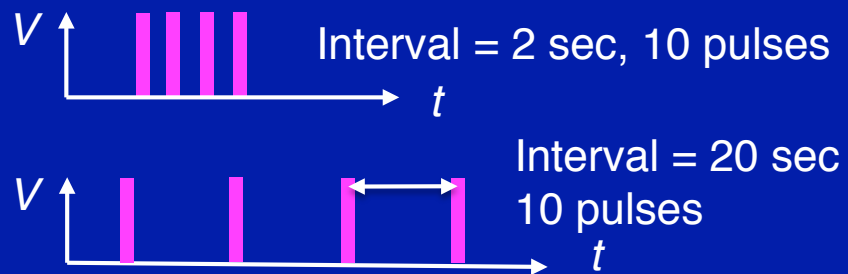
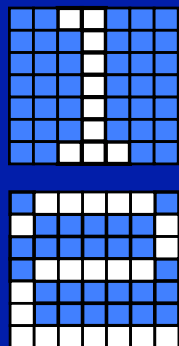
## Initial state

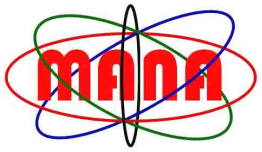


## Stored pattern



- : Long-term memory
- : Short-term memory





# For Logic Application

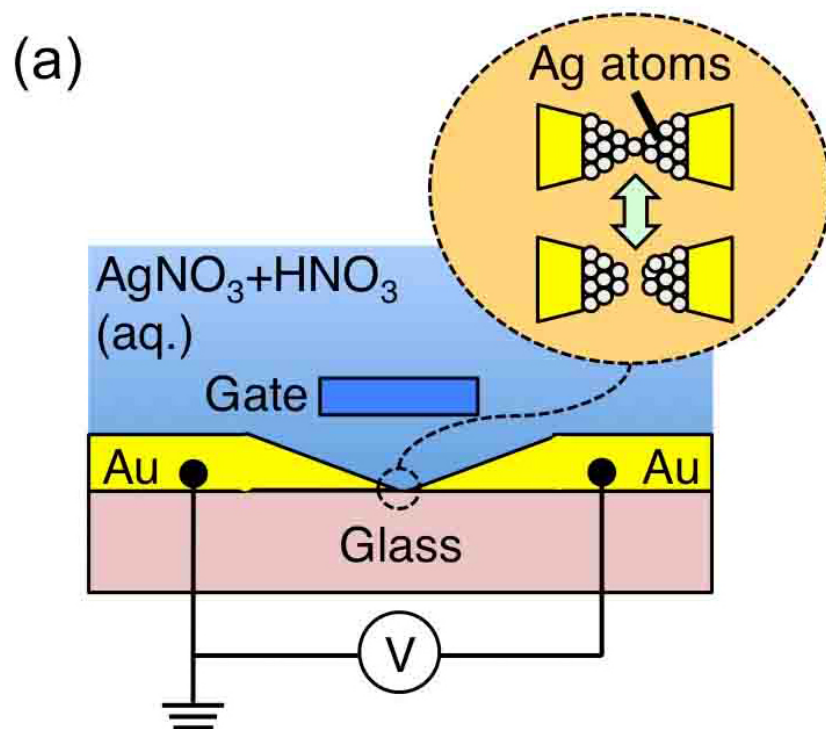


Three-terminal devices are advantageous to two-terminal devices.

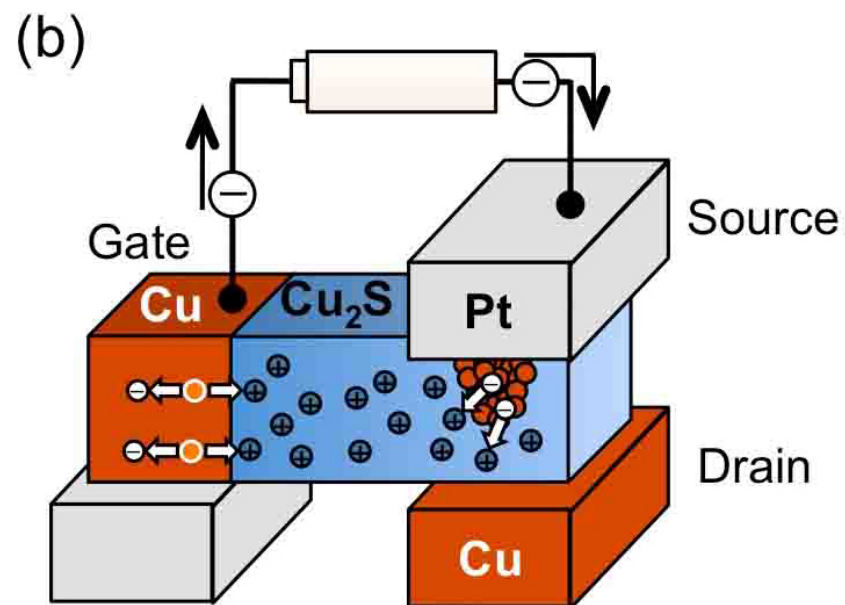
Nonvolatile three-terminal devices are key for low-energy computing systems.

# Three-terminal switches controlling metal filament formation

Filament formation and annihilation are controlled  
such as in the two-terminal atomic switches

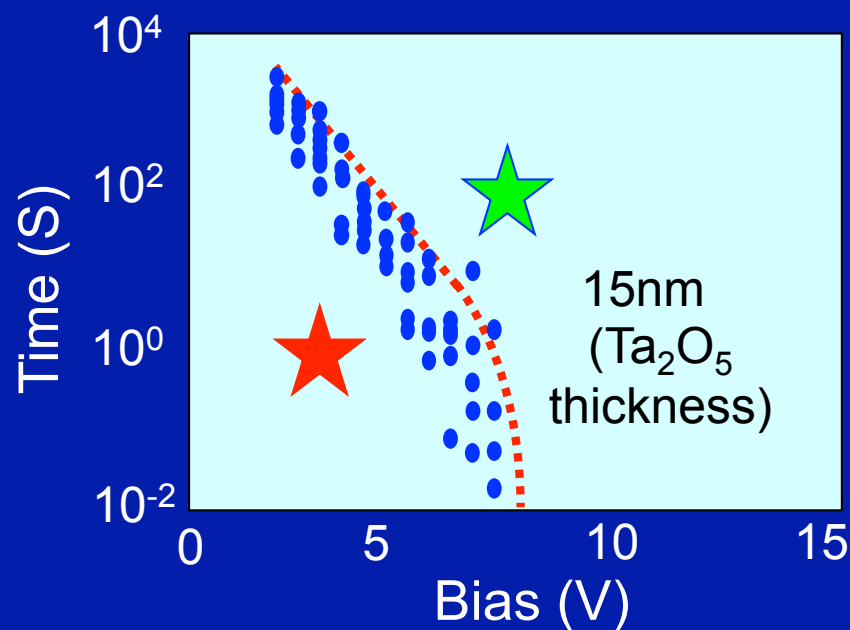


F-Q. Xie et al.,  
Phys. Rev. Lett., 93 (2004) 128303.



N. Banno et al., IEICE Trans.  
Electro., E89-C 11 (2006) 1492.

switching time in the forming process of  
Cu/Ta<sub>2</sub>O<sub>5</sub>/Pt gapless atomic switch

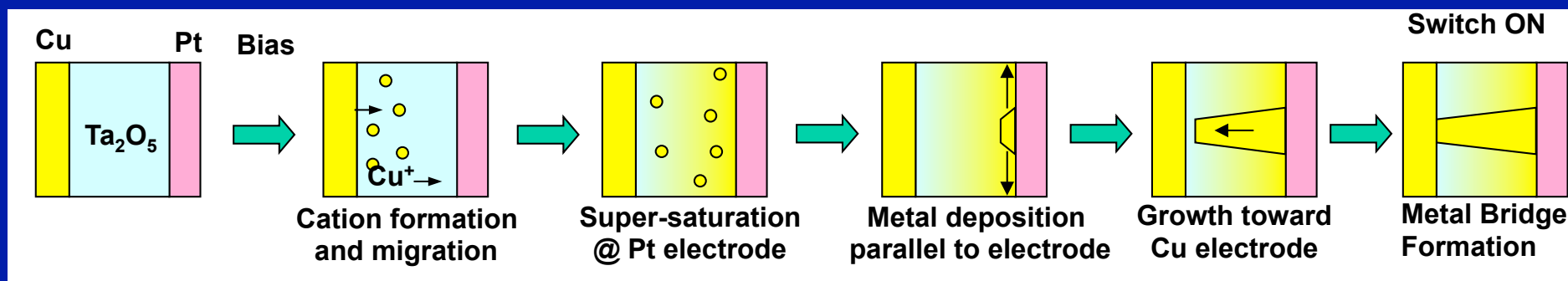


★ : Switching occurs

★ : No switching

As far as controlling filament growth,  
state variable is not bias voltage.

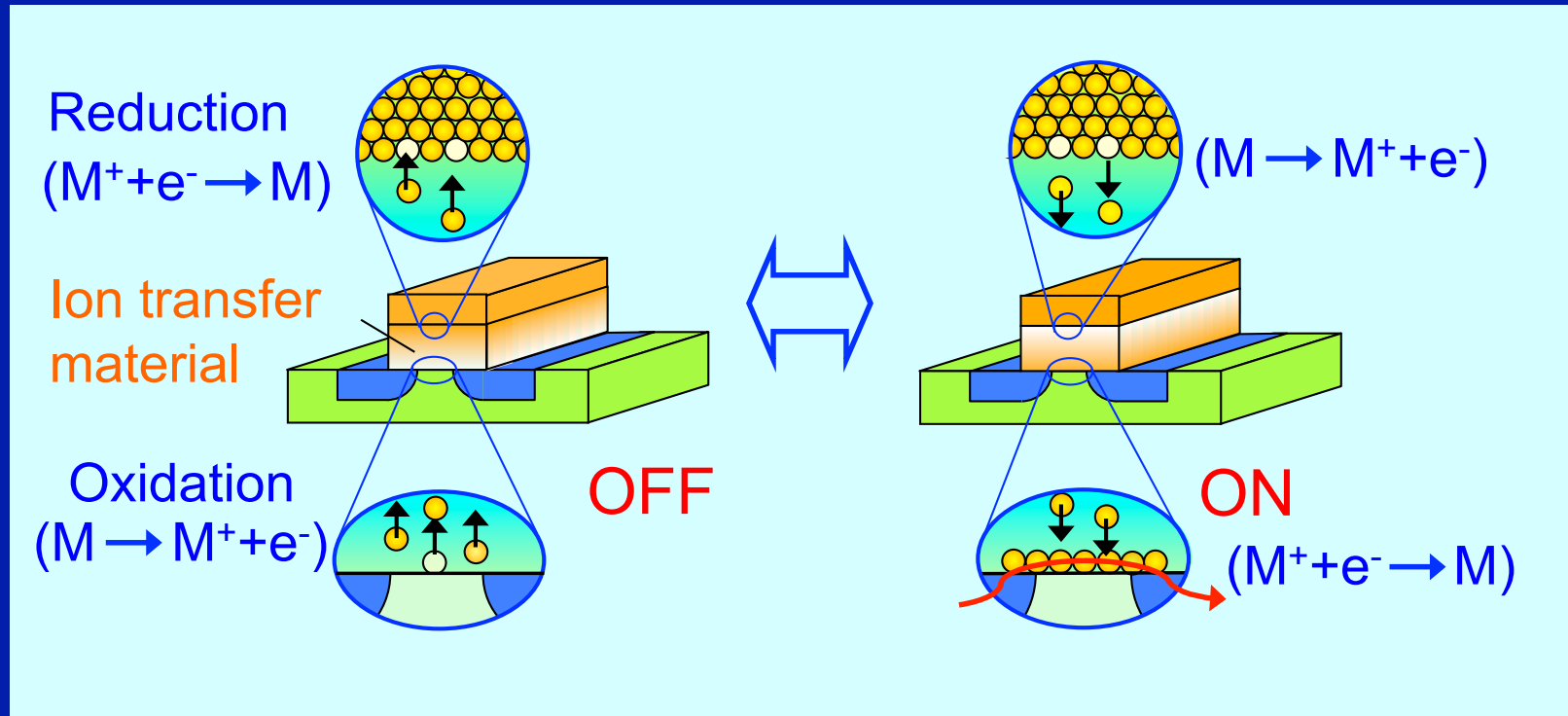
T. Tsuruoka et al., Nanotechnol., 21 (2010) 425205.





# Atom Transistor

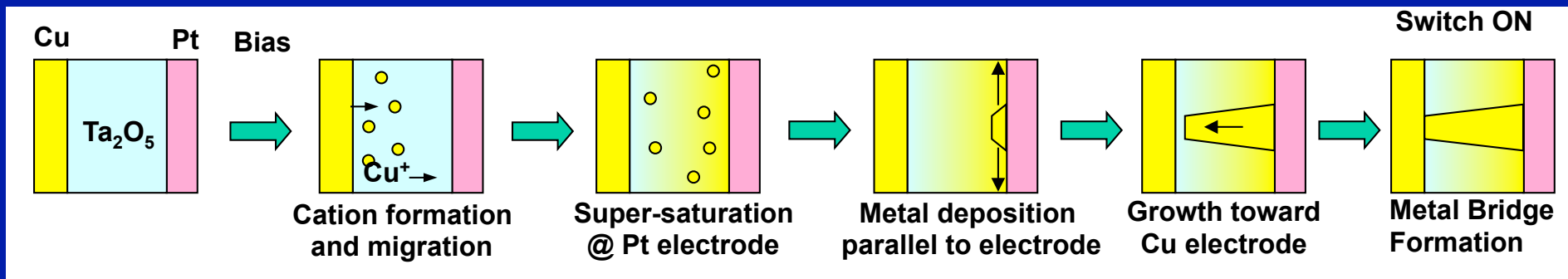
## 3-terminal atomic switch



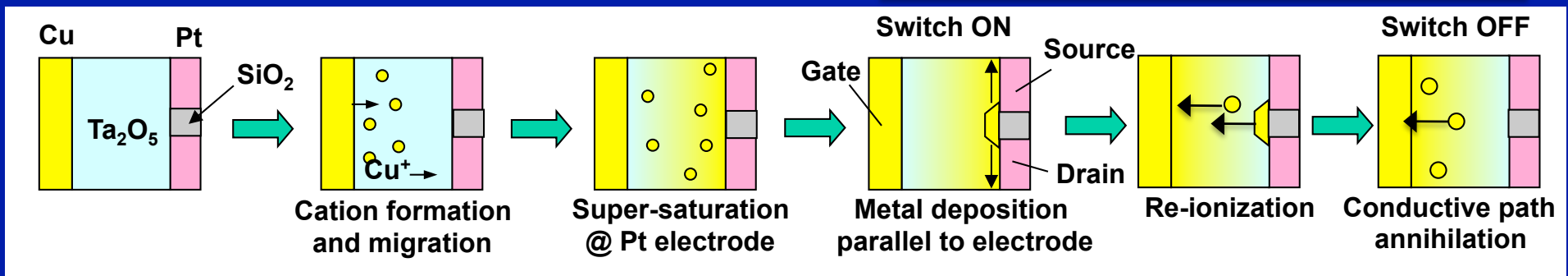
*T. Hasegawa et al., APEX 4, 015204 (2011).*

*Metal cations brought to the channel region make a conductive path.  
Reduction to metal atoms is the origin for the nonvolatility.*

# Operating mechanism of Atom Transistor

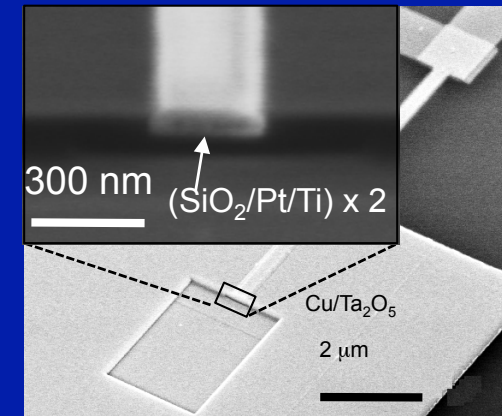
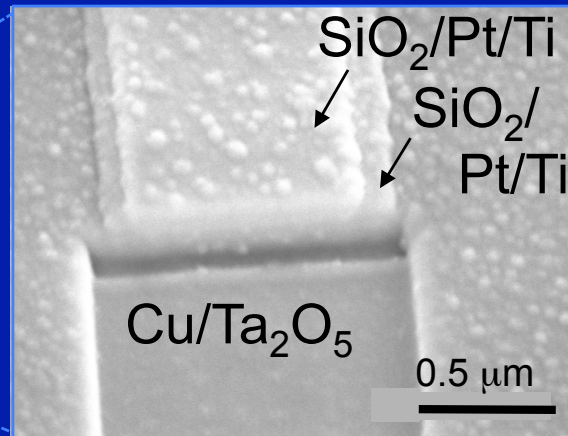
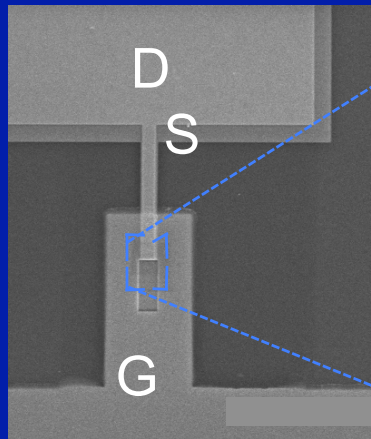


State variable can be bias



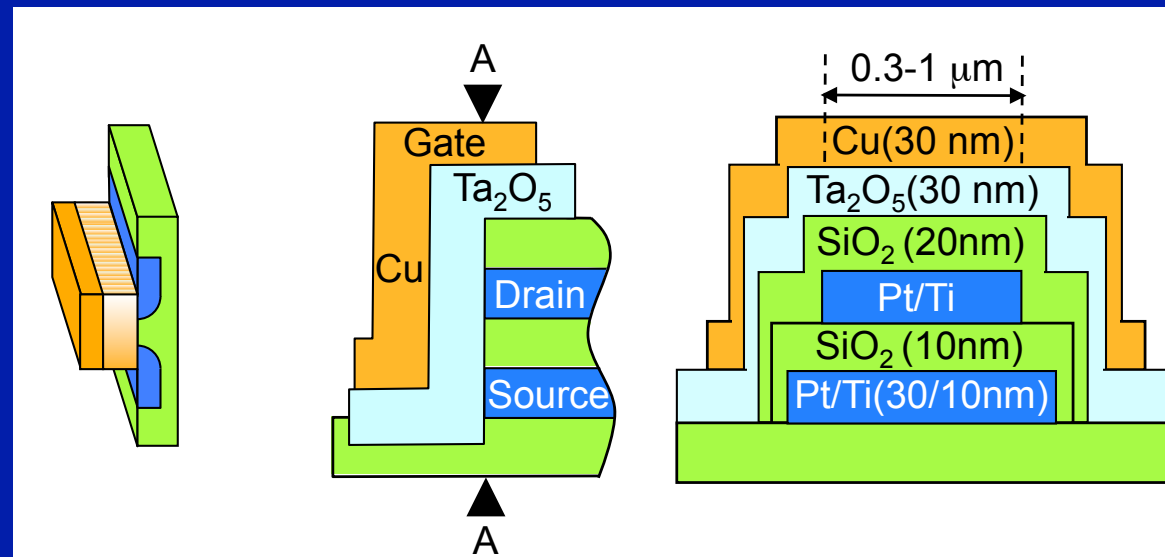
Super-saturation induced conductive path formation.

T. Hasegawa et al., APEX, 4 (2011) 015204.



SEM images of the Atom Transistor

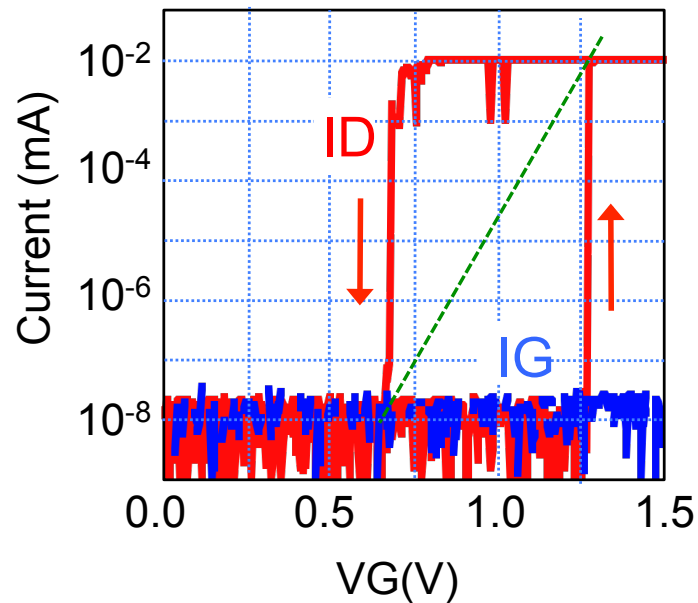
After the side-wall formation



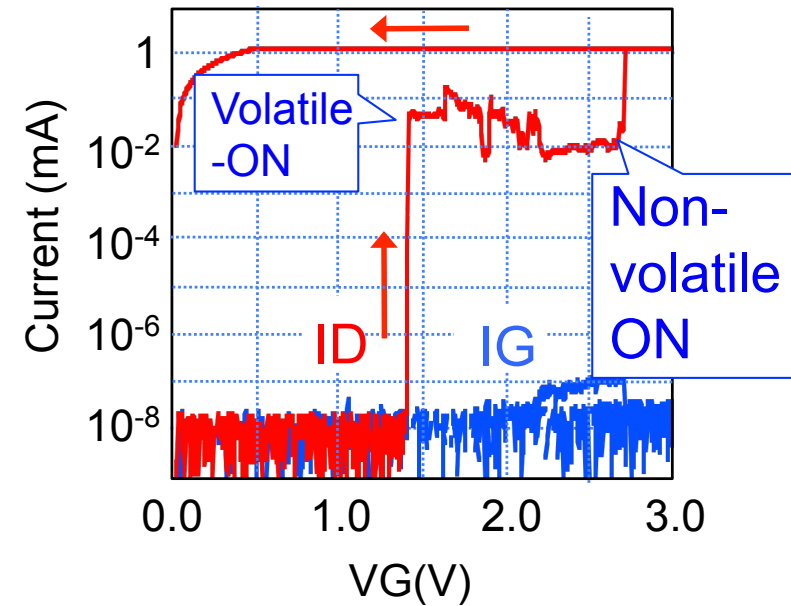
Schematic illustration of Atom Transistor and its cross-sections.

# Operating results of Cu/Ta<sub>2</sub>O<sub>5</sub>/Pt,Pt Atom Transistor

(a) Volatile operation



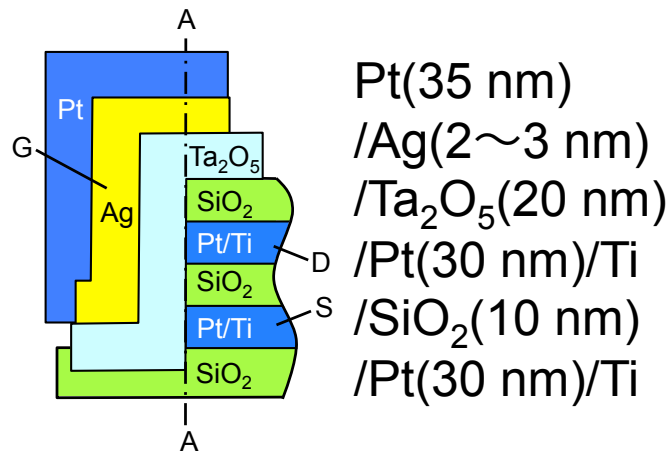
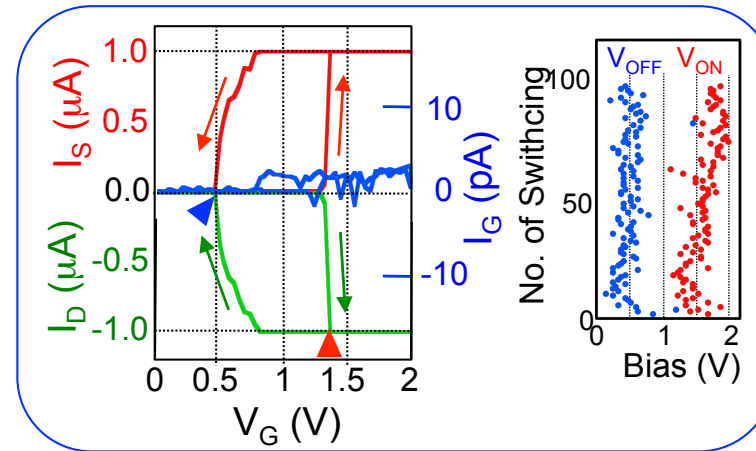
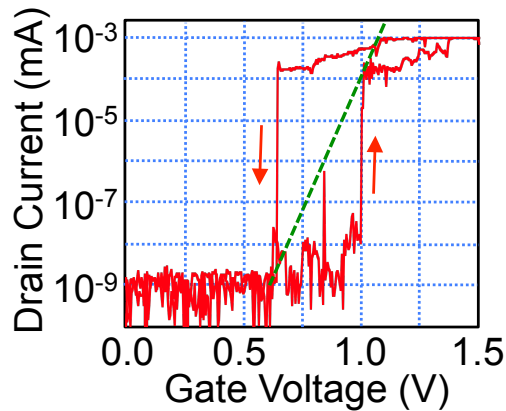
(b) Non-volatile operation



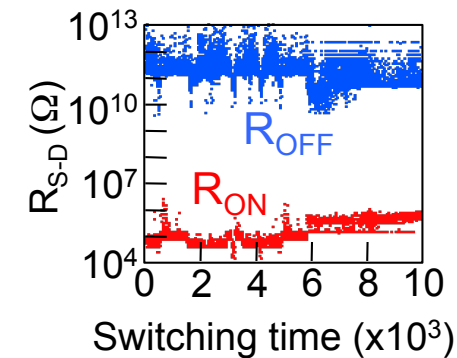
- **Volatile** and **non-volatile** operations depending on the gate bias range
- **I<sub>G</sub> remains small** both in the volatile and non-volatile operations.
- High ON/OFF ratios, **10<sup>6</sup>** for volatile and **10<sup>8</sup>** for nonvolatile operations.

# Operating results of Ag/Ta<sub>2</sub>O<sub>5</sub>/Pt, Pt Atom Transistor

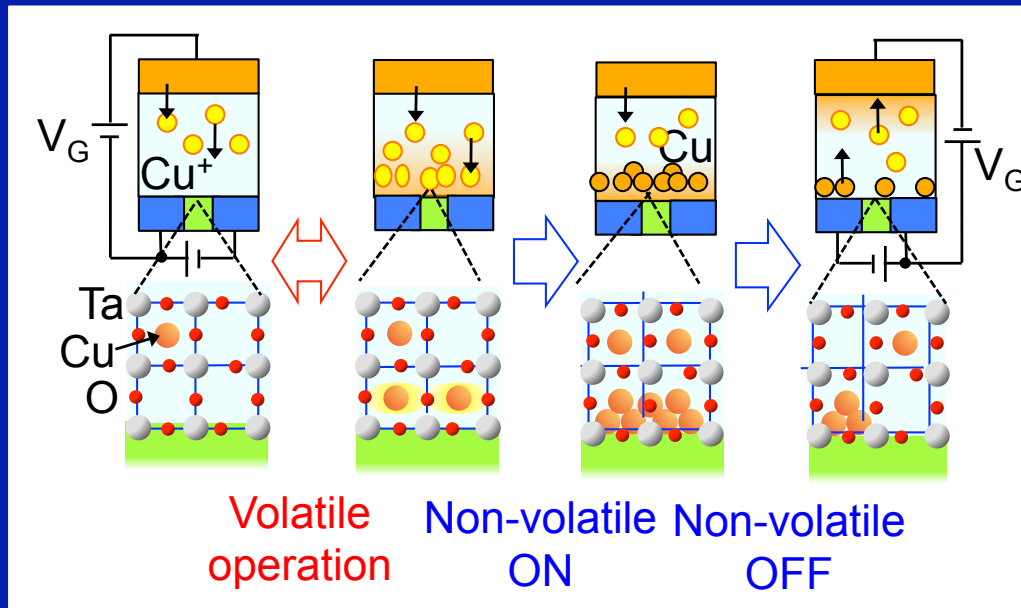
Volatile operations (ON/OFF ratio : 10<sup>6</sup>)



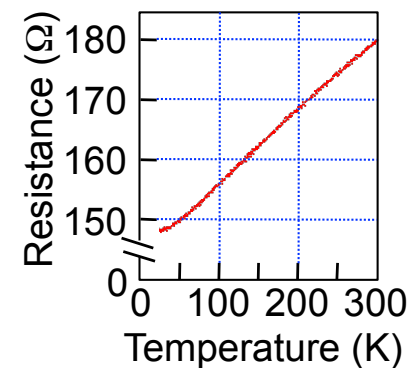
cyclic endurance test



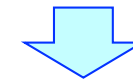
# What is the operating mechanism for the volatile and non-volatile operations?



Temp. dependence of nonvolatile  $R_{ON}$



Measured by I/V sweeping between  $\pm 1V$



Conductive path should consist of metal atoms

Reduction/ionization of metal cations/atoms at the S-D electrodes should cause the non-volatile operation.

Atom transistor is an atom movement controlled three-terminal nanodevice. It shows volatile and non-volatile operations with very low power consumption. It may achieve the downscaling further than 11 nm technology node.

